## DECLARATION FOR PATENT APPLICATION (WITH POWER OF ATTORNEY)

As an inventor named below or on any attached continuation page, I hereby declare that:
My residence, post office address and citizenship are as stated next to my name.
I believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled UNDERFILLED, ENCAPSULATED SEMICONDUCTOR DIE ASSEMBLIES AND METHODS OF FABRICATION, the specification of which (check one):

is attached hereto.

 $\boxtimes$ 

	as filed on		United States application	serial no.	and
	was amended or as filed on was amended ur	as P	CT international applicate 19 on	tion no	and
I here specification	eby state that I have, including the clai	e reviewed and t ms, as amended	understand the contents or by any amendment refer	of the aboverred to abor	e-identified ve.
information l	known to me to be	material to the p	e U.S. Patent and Trader patentability of the subject 37, Code of Federal Reg	ct matter cl	laimed in this
or § 365(b) or PCT internat America listed on any attach any PCT internation	of any foreign applitional application(s) and below and on an led continuation particational applicational	cation(s) for pat designating at y attached conti- ge any foreign a on(s) designating	under Title 35, United Steent or inventor's certific least one country other the nuation page and have a application for patent or it at least one country other application(s) on which the application(s) on which	ate or § 36 han the Un lso identification inventor's controller than the	5(a) of any ited States of ed below and certificate or United States
Prior foreign/	PCT application(s)	):		Priority	Claimed
(numb	per)	(country)	(day/month/year filed)	Yes	No
(numb	per)	(country)	(day/month/year filed)	Yes	No
				-	

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) or § 365(c) of PCT international application(s) designating the United States of America listed below and on any attached continuation page and, insofar as the subject matter of each of the claims of this application is not disclosed in any such prior application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose to the U.S. Patent and Trademark Office all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations § 1.56 which became available

## **DECLARATION FOR PATENT APPLICATION**

(continuation page)

Invention Title: UNDERFILLED, ENCAPSULATED SEMICONDUCTOR DIE ASSEMBLIES AND METHODS OF FABRICATION

between the filing date of such price of this application:	or application and the nati	onal or PCT international filing date
(application serial no.)	(filing date)	(status-pending, patented or abandoned)
(application serial no.)	(filing date)	(status-pending, patented or abandoned)
I hereby claim the benefit un States provisional application(s) list		es Code, § 119(e) of any United
(provisional application no.)	(filing date)	<del></del>

I hereby appoint the following Registered Practitioners to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith:

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these

## DECLARATION FOR PATENT APPLICATION

(continuation page)

Invention Title: UNDERFILLED, ENCAPSULATED SEMICONDUCTOR DIE ASSEMBLIES AND METHODS OF FABRICATION

statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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